

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Jay B. Reimer, et al.

Serial No: 10/008,696

Filed: October 30, 2001

For: Multicore DSP Device Having Coupled Subsystem Memory Buses for Global DMA Access



TI-30105

Art Unit: 2185

Examiner:

Conf. No.: 7469

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INFORMATION DISCLOSURE STATEMENT

Ass't Commissioner for Patents

Washington, DC 20231

Dear Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Ass't Commissioner for Patents, Washington, D.C. 20231 on May 3, 2002.


Robin E. Barnum

Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed PTO-1449. Copies of the noted references are enclosed herewith. The relevancy of each of the references is noted on the enclosed Search Reports.

Since this IDS is being submitted before any Office Action, no fee is due.

Respectfully submitted,



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